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CONFIRMATION NO. 9538

Bib Data Sheet

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|-----------------------------|---------------------------------------|--------------|------------------------|---------------------------------------|
| SERIAL NUMBER 10/765,488 | FILING DATE 01/27/2004 RULE | CLASS 365 | GROUP ART UNIT 2818 | ATTORNEY DOCKET NO. NETL.001DV3 |
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APPLICANTS

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** CONTINUING DATA *****
 This application is a DIV of 10/094,512 03/07/2002 PAT 6,751,113 *yes 4*

** FOREIGN APPLICATIONS ***** *no 4*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
 ** 06/15/2004

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|--|---|---------------------------|------------------------|-----------------------|----------------------------|
| Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged | <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature <i>[Signature]</i> Initials <i>[Initials]</i> | STATE OR COUNTRY CA | SHEETS DRAWING 9 | TOTAL CLAIMS 21 | INDEPENDENT CLAIMS 3 |
|--|---|---------------------------|------------------------|-----------------------|----------------------------|

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TITLE
 Arrangement of integrated circuits in a memory module

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